

Figure 1: System Schematic diagram

# **CCD timing diagram**

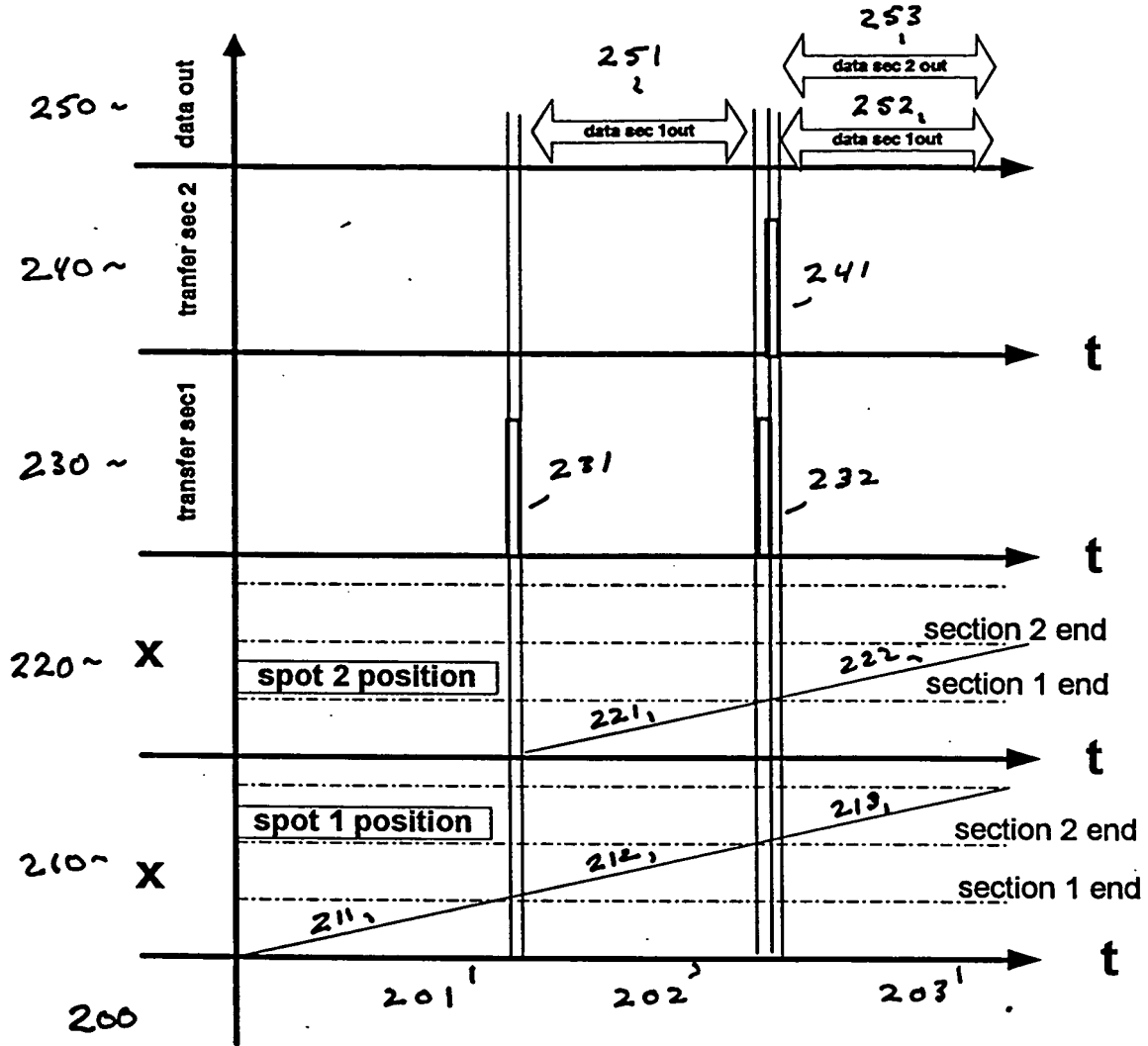
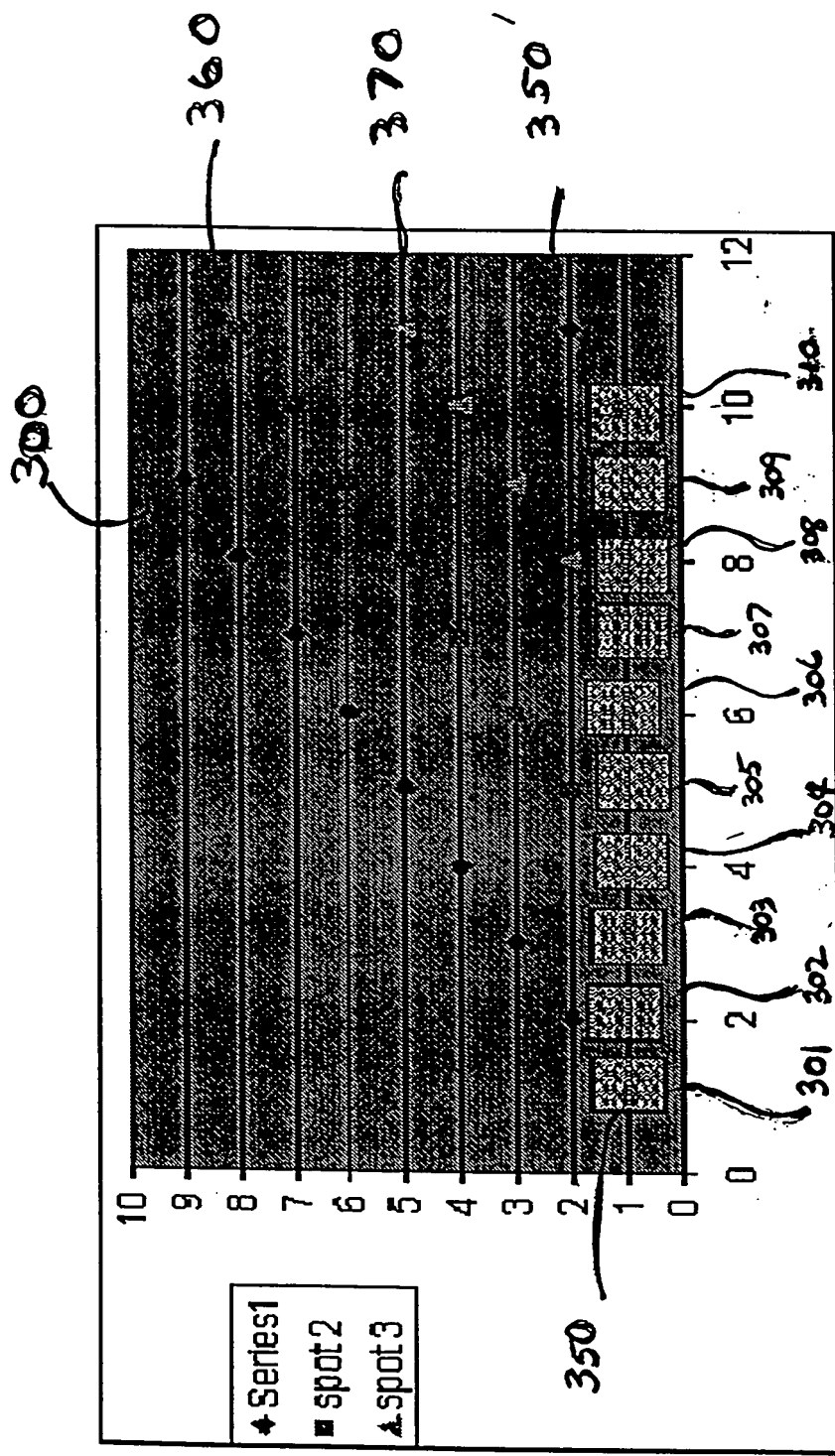


Figure 2: Timing diagram



CCD

Fig 3

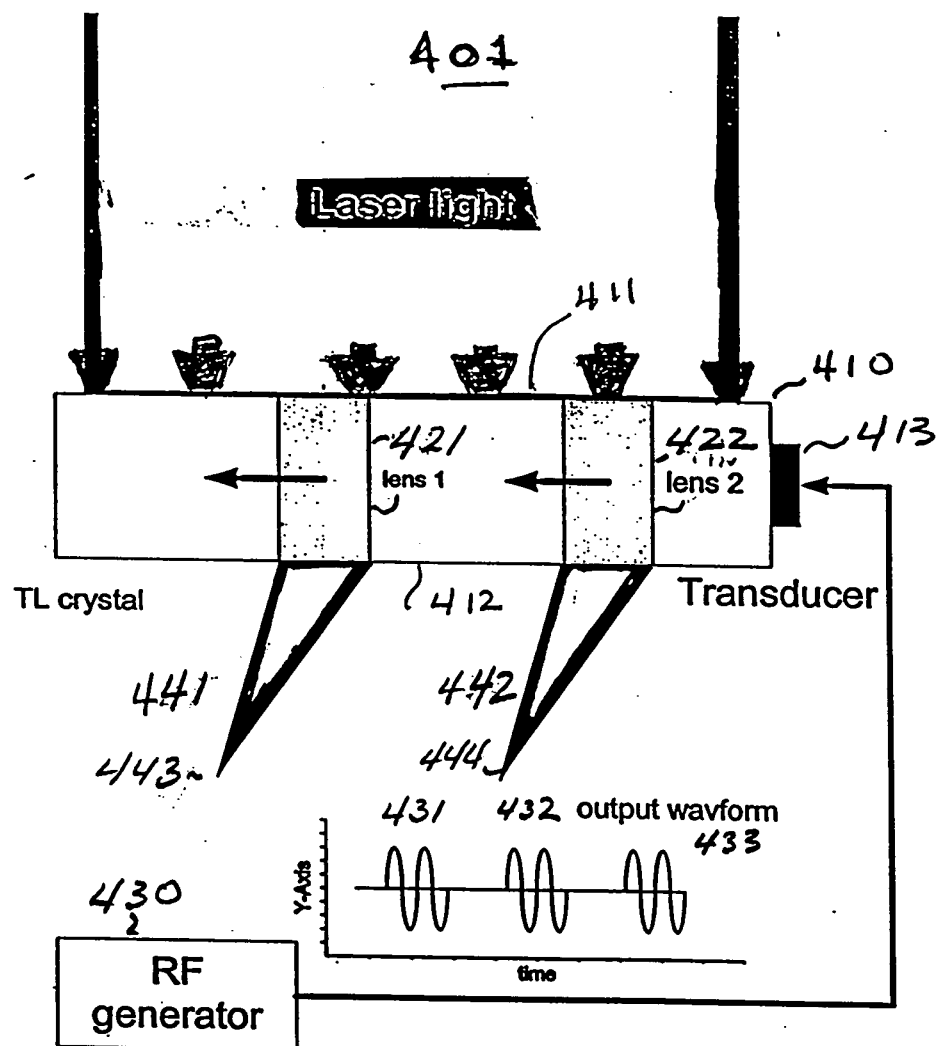


FIG. 4

500

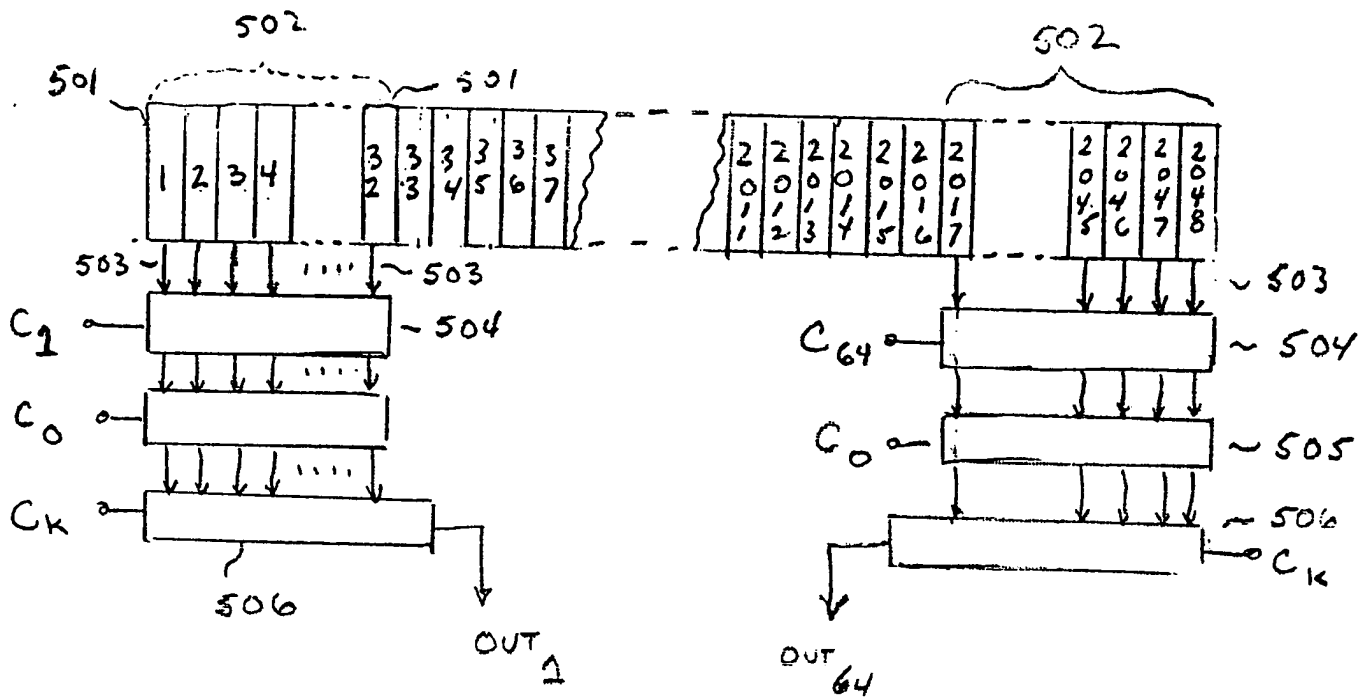
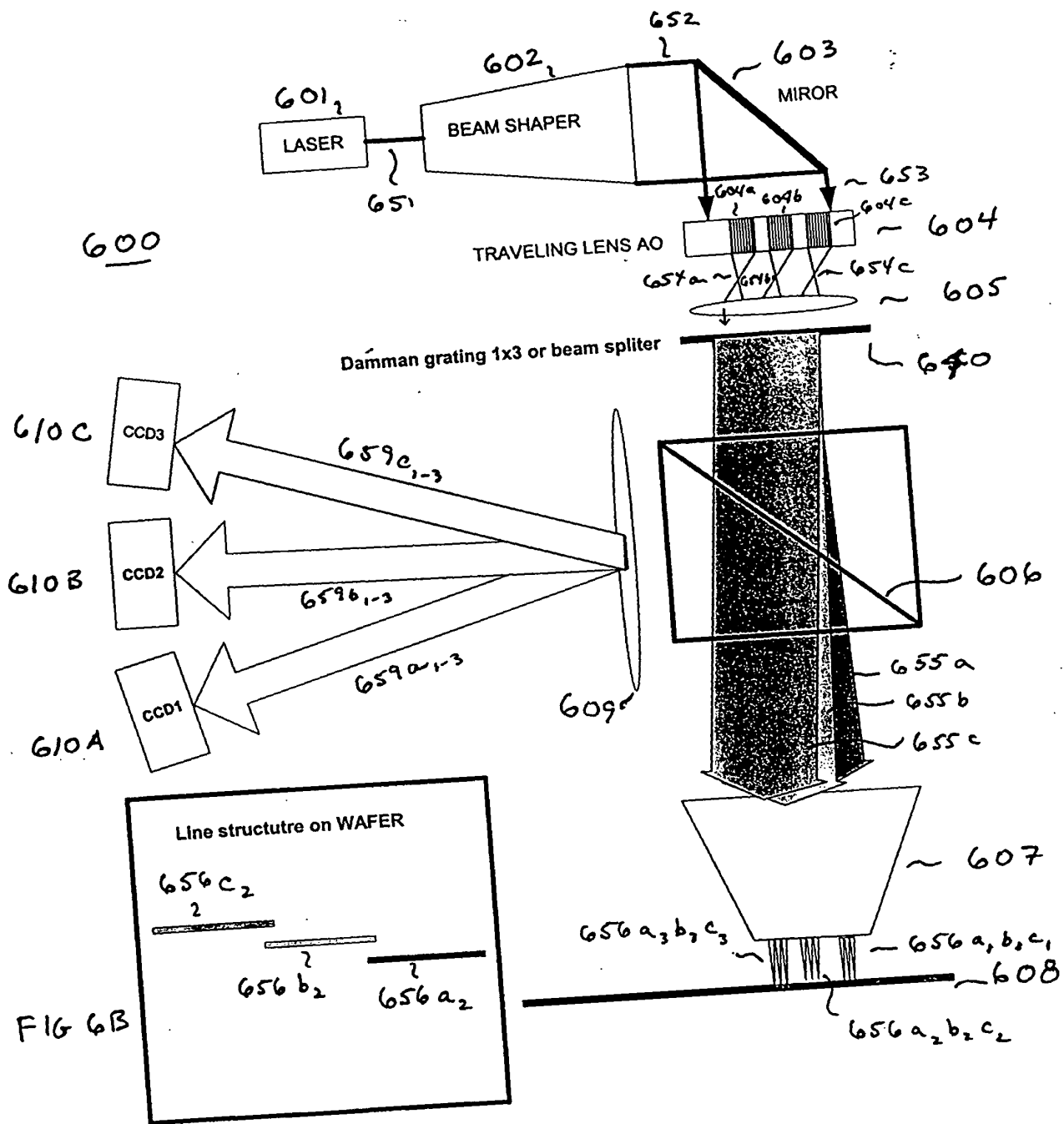


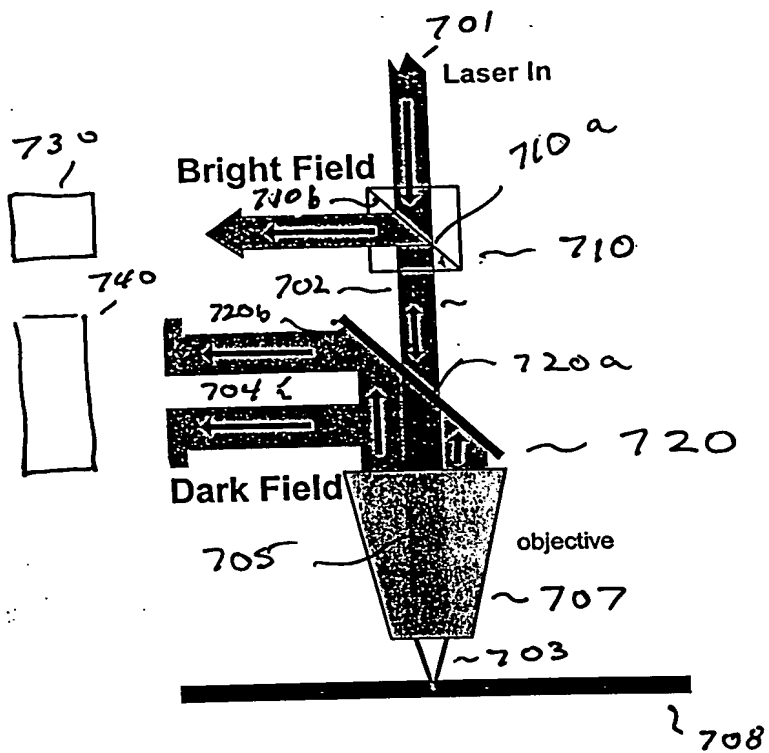
FIGURE 5

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**FIG 6A**

700



750

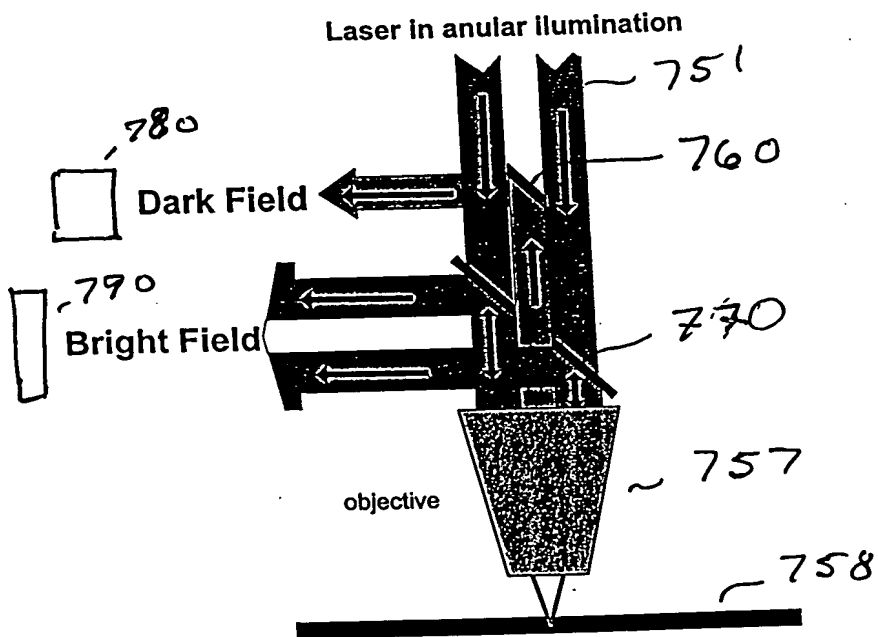


FIG 7B